

Express Mail Label No. EV329723944US Inventor: Dorr Docket No. 130-018 Dual Master Dual Serial Bus Architecture Mailing date: September 29, 2003 16b 16a Page 2 of 10 system monitor 1 system monitor 0 G and 1:15 TO A COMPAN data mover data mover Changa a graff 12 data mover data mover Sec 800 [28] [43, 110] 1246 DO THY FB \* Sw. mla 1 124 120 111 A. ..... is rite Saf Bring CS<sub>1</sub> CS<sub>0</sub> 22b 22a 14b 20b 14a 20a

Fig. 2

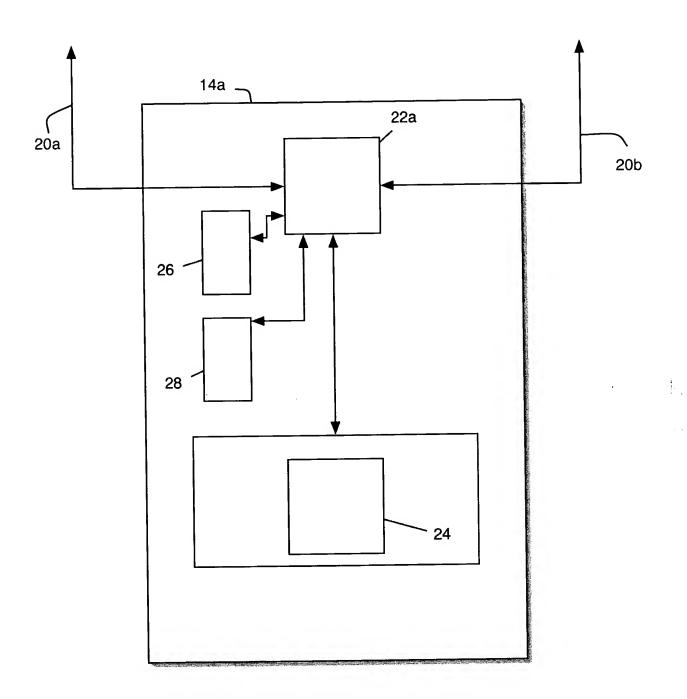


Fig. 3

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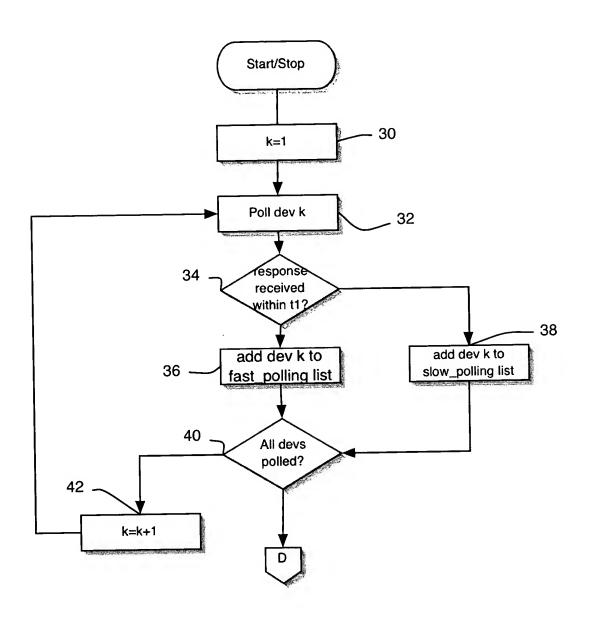


Fig. 4A

Express Mail Label No. EV329723944US Inventor: Dorr Docket No. 130-018 Dual Master Dual Serial Bus Architecture Mailing date: September 29, 2003 Page 5 of 10 44 n=1,p=1 Poll dev 46 fast\_poll\_list<n> 54 esponse received 48 increment within t? no\_rsp\_cnt<n> Y 52 Ν no\_rsp\_cnt< n=last? n> >= T? n=n+1Y add dev n to Poll dev slow\_polling list slow\_poll\_list 60 n=1

62

64

p=p+1

70

66

56

58

Fig. 4B

68

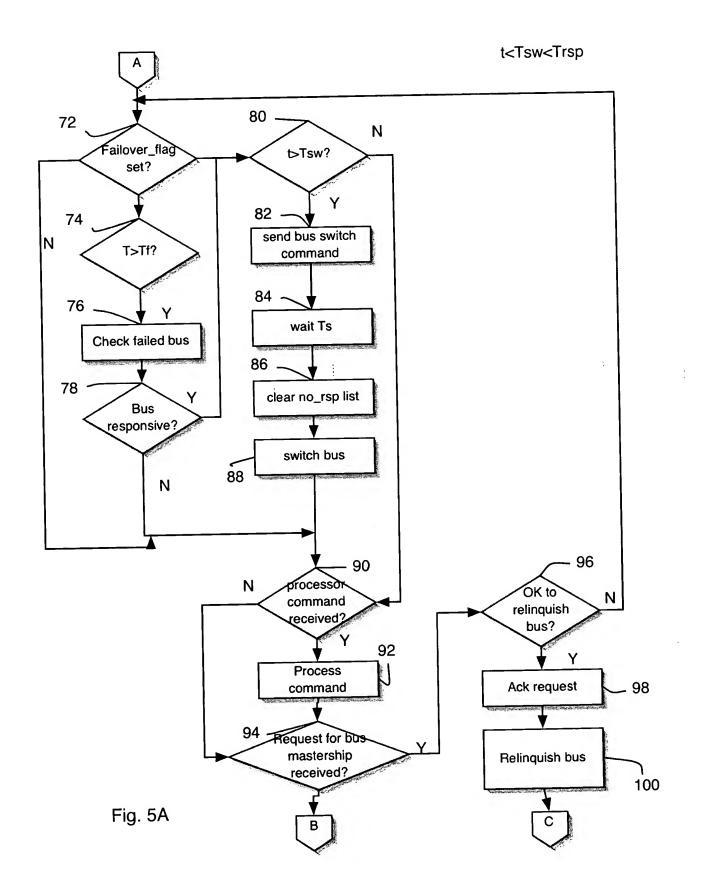
esponse

received within t?

add dev to fast\_poll\_list

p=last?

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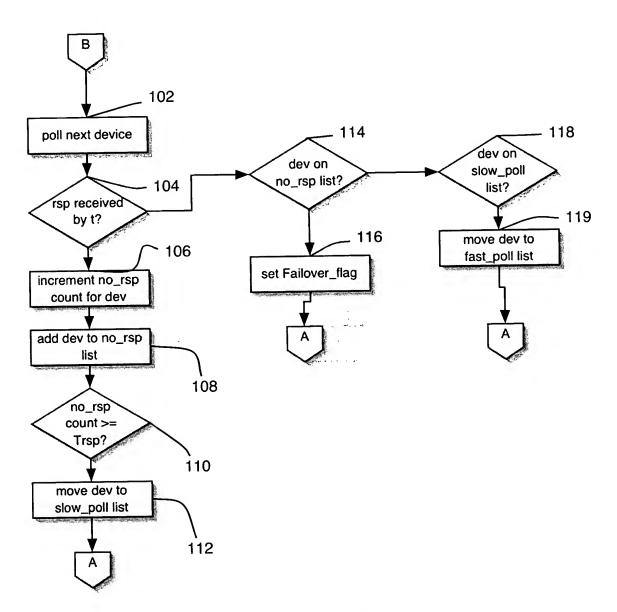


Fig. 5B

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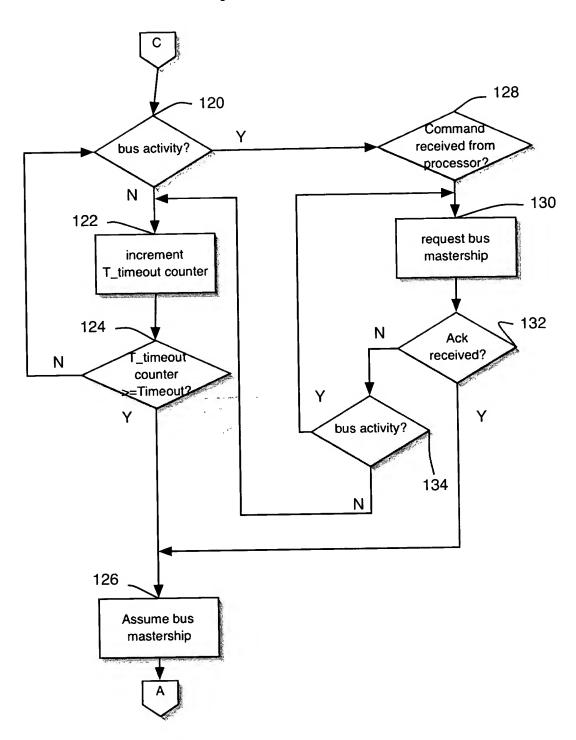
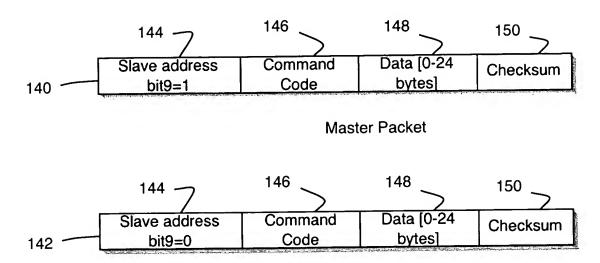


Fig. 6

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Slave Packet

Fig. 7

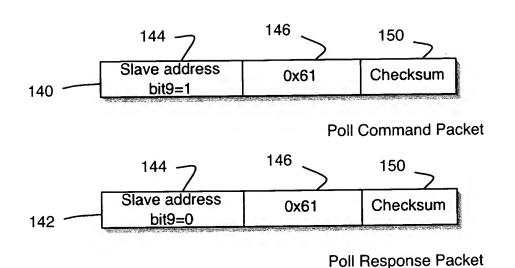
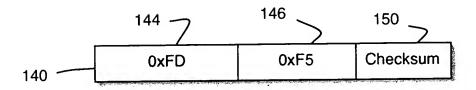
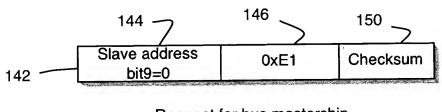


Fig. 8

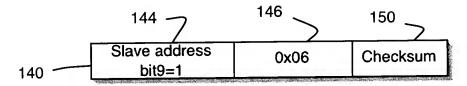


Broadcast Packet - switch bus

Fig. 9



Request for bus mastership



Acknowledge (ACK)

Fig. 10